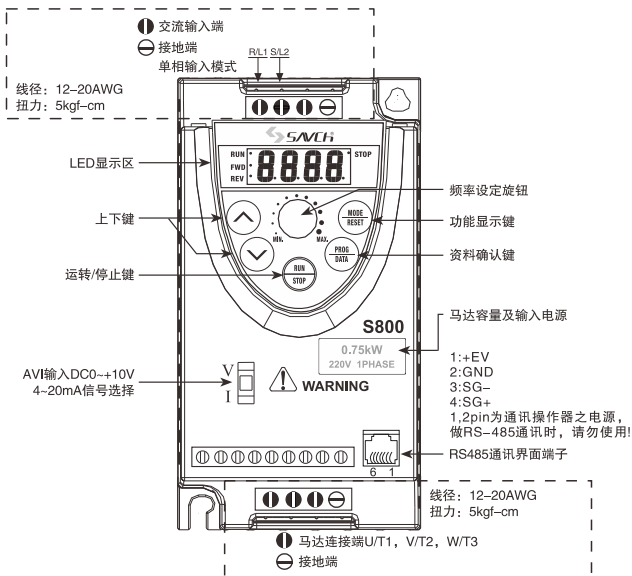


# 主回路接线



# 控制回路配线图

